

AMENDMENTS TO THE CLAIMS

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**Claim 1** (Currently Amended) An anti-alias font generator comprising:

a stipple buffer for holding gradation data of an anti-alias font transferred from a CPU to the stipple buffer;

a source color register for setting a font display color; and

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amt*  
a blender for blending a value of said source color register and a destination color value on a frame memory in accordance with a blend coefficient which is the gradation data held in the stipple buffer.

**Claim 2** (Original) An anti-alias font generator as claimed in claim 1, wherein said blender blends the source color register value and the destination color value in accordance with  $\alpha \times Cs + (1 - \alpha) \times Cd$  assuming that the anti-alias font bit map gradation data value held in the stipple buffer is  $\alpha$ , and the value of said source color register is  $Cs$  and the destination color value on said frame memory is  $Cd$ .

**Claim 3** (Currently Amended) An anti-alias font generator comprising:

a stipple buffer for holding gradation data of an anti-alias font transferred from a CPU to the stipple buffer;

a foreground color register for setting a font display color;

a background color register for setting a background color;

and

a blender for blending a font display color of said foreground color register and the background color of said background color register in accordance with ~~the~~ a blend coefficient which is the gradation data held in the stipple buffer.

**Claim 4** (Original) An anti-alias font generator as claimed in claim 3, wherein said blender blends the source color register value and the destination color value in accordance with  $\alpha \times C_f + (1 - \alpha) \times C_b$  assuming that the anti-alias font bit map gradation data value held in said stipple buffer is  $\alpha$ , the value of said foreground color register is  $C_f$  and the value of the background register is  $C_b$ .

**Claim 5** (Currently Amended) An anti-alias font generator, comprising:

a stipple buffer for holding gradation data of an anti-alias font;

a plurality of display color registers for setting a display color on the basis of the gradation value of said anti-alias font;  
and

*ant* a stipple color selector for selecting ~~the~~ a value of said plurality of display color registers in accordance with said gradation data held in the stipple buffer.

**Claim 6** (Original) An anti-alias font generator as claimed in claim 5, wherein said plurality of display color registers are structured by a first foreground color register, a second foreground color register, a third foreground color register and a background color register, and said stipple color selector selects a display color from said first foreground color register if the anti-alias font bit map gradation data is first gradation data, a display color from said second foreground color register if the anti-alias font bit map gradation data is second gradation data, a display color from said third foreground color register if the anti-alias font bit map gradation data is third gradation data, and a display color from said background color register if the anti-alias font bit map

gradation data is fourth gradation data, in accordance with the anti-alias font bit map gradation data in said stipple buffer.

**Claim 7** (New) The anti-alias font generator of claim 1, wherein the blender loads the blend coefficient from the stipple buffer.

*Al added*  
**Claim 8** (New) The anti-alias font generator of claim 3, wherein the blender loads the blend coefficient from the stipple buffer.

**Claim 9** (New) The anti-alias font generator of claim 1, wherein the anti-alias <sup>112</sup>generator is a hardware accelerator.

**Claim 10** (New) The anti-alias font generator of claim 12, wherein the hardware accelerator is coupled to the CPU to receive the gradation data.

**Claim 11** (New) The anti-alias font generator of claim 3, wherein the anti-alias generator is a hardware accelerator.

**Claim 12** (New) The anti-alias font generator of claim 5, wherein the anti-alias generator is a hardware accelerator.

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